

SC12752TP

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Substitute for form 1449A/PTO				Complete if Known	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(use as many sheets as necessary)</i>				Application Number	10687271
				Filing Date	October 16, 2003
				First Named Inventor	Clubunmi O. Adewumi et al.
				Group Art Unit	2813
				Examiner Name	Laura M. Schillinger
Sheet	1	of	1	Attorney Docket Number	SC12752TP

U. S. PATENT DOCUMENTS

FOREIGN PATENT DOCUMENTS

Examiner Signature	<i>Naomi Schelp</i>	Date Considered	<i>1/22/06</i>
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Substitute for form 1449A/PTO		Complete if Known	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT		Application Number	10697,271
(use as many sheets as necessary)		Filing Date	October 16, 2003
Sheet	of	First Named Inventor	Otubunmi O. Adetutu et al.
		Group Art Unit	2819
		Examiner Name	Laura M. Schillinger
		Attorney Docket Number	SC12752TP

OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
<i>Liu</i>	BH	Byeong-Ok Cho et al., "Tuning the Electrical Properties of Zirconium Oxide Thin Films," <i>Applied Physics Letters</i> , February 11, 2002, Vol. 80, No. 6, pp. 1052-1054.	
<i>J</i>	BI	A. L. P. Rotondaro, et al., "Advanced CMOS Transistors with a Novel HfSiON Gate Dielectric," <i>2002 Symposium on VLSI Technology Digest of Technical Papers</i> , Section 15.2.	
<i>J</i>	BJ	Yuji Saito, et al., "Advantage of Radical Oxidation for Improving Reliability of Ultra-Thin Gate Oxide," <i>2002 Symposium on VLSI Technology Digest of Technical Papers</i> .	
<i>Liu</i>	BK	M. Togo, et al., "Low-Leakage and Highly-Reliable 1.5 nm SiON Gate-Dielectric Using Radical Oxynitridation for Sub-0.1 μm CMOS," <i>2000 Symposium on VLSI Technology Digest of Technical Papers</i> .	

Examiner Signature	<i>Laura M. Schillinger</i>	Date Considered	1/22/06
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INFORMATION DISCLOSURE STATEMENT BY APPLICANT				<i>Complete If Known</i>		
(use as many sheets as necessary)				Application Number 10/687,271 Filing Date 10/16/03 First Named Inventor Olubunmi O. Adetulu Group Art Unit 2813 Examiner Name Attorney Docket Number SC12752TP		
U. S. PATENT DOCUMENTS						
Examiner Initials*	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	
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<i>JL</i>	AA	Al-Shareef <i>et al.</i> , "Device Performance of <i>in-situ</i> Steam Generated Gate Dielectric Nitrided by Remote Plasma Nitridation," <i>Applied Physics Letters</i> , June 11, 2001, Vol. 78, No. 24, pp. 3875-3877/				
<i>JL</i>	AB	Nicollian <i>et al.</i> , "Extending Reliability Scaling Limit of SiO ₂ Through Plasma Nitridation," <i>IEDM Tech. Dig.</i> , 2000, pp. 545-548.				
<i>JL</i>	AC	Tseng <i>et al.</i> , "Ultra-Thin Decoupled Plasma Nitridation (DPN) Oxynitride Gate Dielectric for 80-nm Advanced Technology," <i>IEEE Electron Device Letters</i> , December 2002, Vol. 23, No. 12, pp. 704-706.				

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